

**GUIDELINES of B.Sc. (H) Computer Science
(Under Choice Based Credit Scheme) LOCF
BHCS16B
Semester -V**

Microprocessors				
S. No.	Chapter Number and Name	Section Numbers	No. of Lecture*	Chapter wise weightage* (Marks)
1	The Microprocessor and its Architecture	Chapter 2: 2.1-2.3, 2.5	6	10
2	Addressing Modes	Chapter 3	6	10
3	Data Movement Instructions	Chapter 4 (up to 4.5)	5	8
4	Assembly Language	Chapter 5	6	6
5	Program Control Instructions	Chapter 6	6	10
6	Interfacing	Chapter 9 (up to 9.4)	6	6
7	Memory Interfacing	Chapter 10 (up to 10.4)	6	6
8	I/O Interfacing	Chapter 11 11.1 (up to page 384) 11.2, 11.3 (up to page 399, 414-421) 11.4 (up to page 429) 11.5	6	6
9	Interrupts	Chapter 12 (up to page 475)	6	5
10	DMA controller	Chapter 13: 13.1, 13.2	4	5
11	Advanced Microprocessor Architecture	Chapter 18 18.1 (page 734-738) 18.5 (except pin function and pin diagram)	3	3

Reference:

[1] Barry B. Brey: The Intel Microprocessors: Architecture, Programming, and Interfacing, Pearson Education, Eighth Edition

***The number of lectures and weightage may be treated as indicative only**

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